

# HSS8550

**General Purpose Transistors**  
**PNP Silicon**

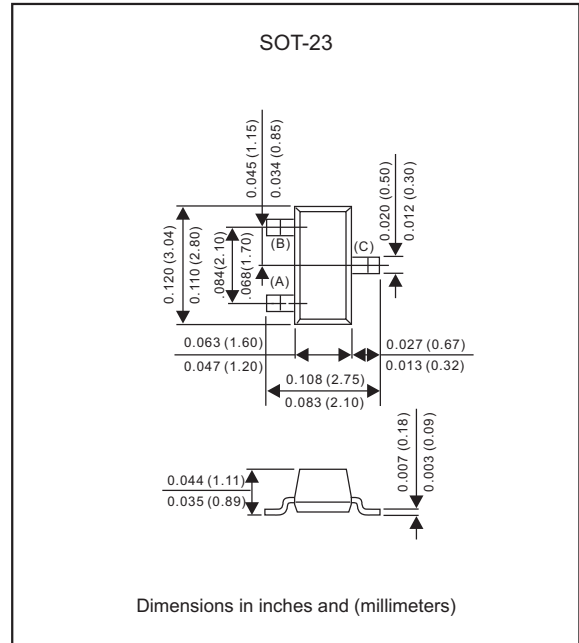
## Features

- High current capacity in compact package  $I_c = -1.5A$ .
- Epitaxial planar type
- Pb-free package is available

## Mechanical data

- Epoxy: UL94-V0 rated flame retardant
- Case : Molded plastic, SOT-23
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Mounting Position : Any
- Weight : Approximated 0.008 gram

## Package outline



## MAXIMUM RATINGS ( $T_A=25^{\circ}C$ unless otherwise noted)

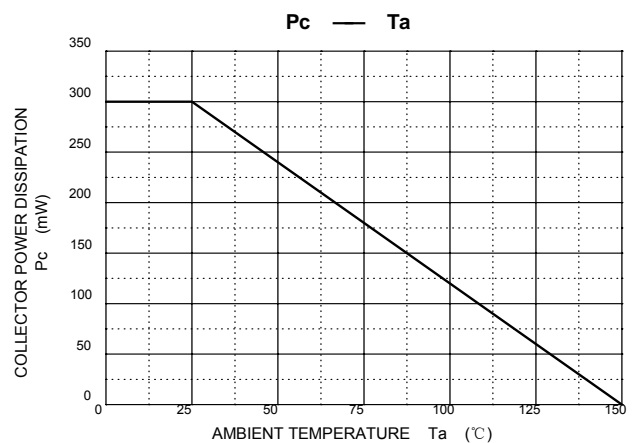
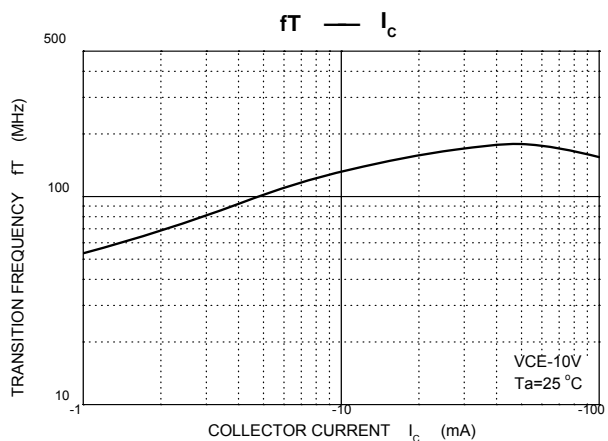
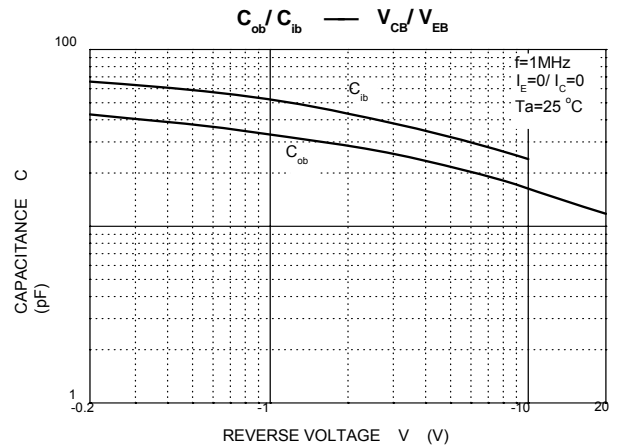
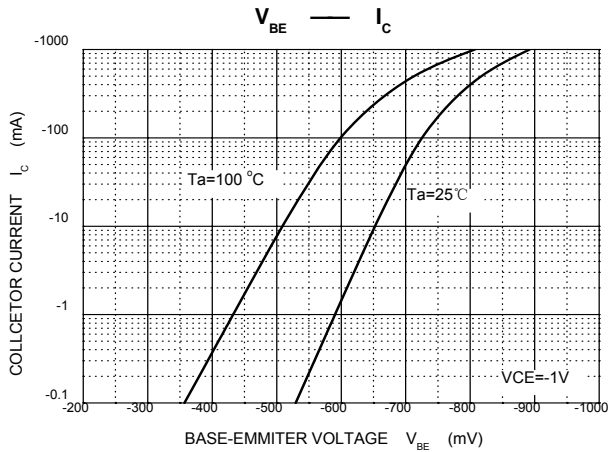
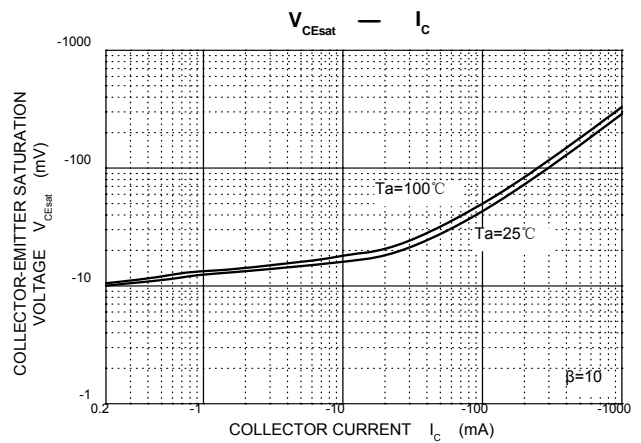
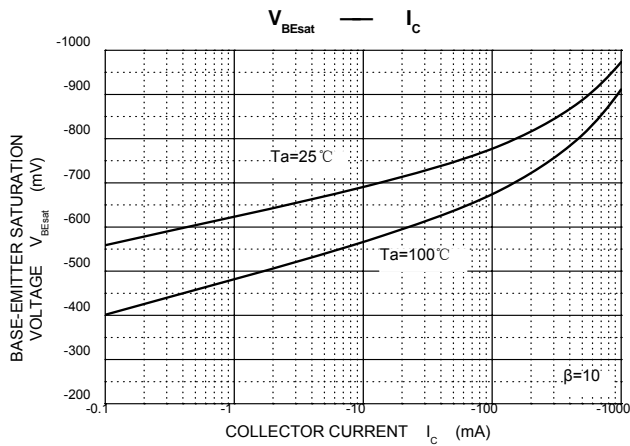
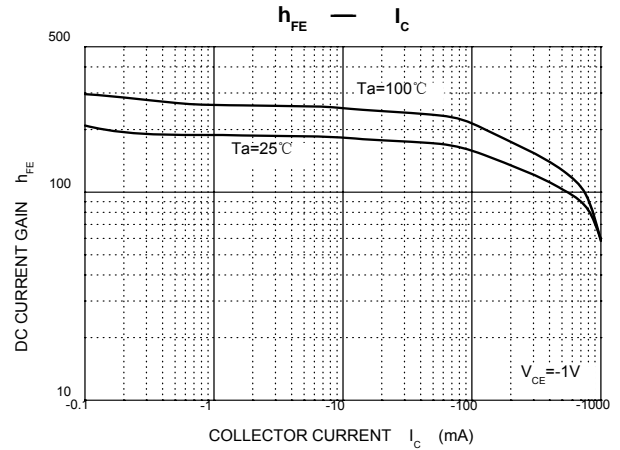
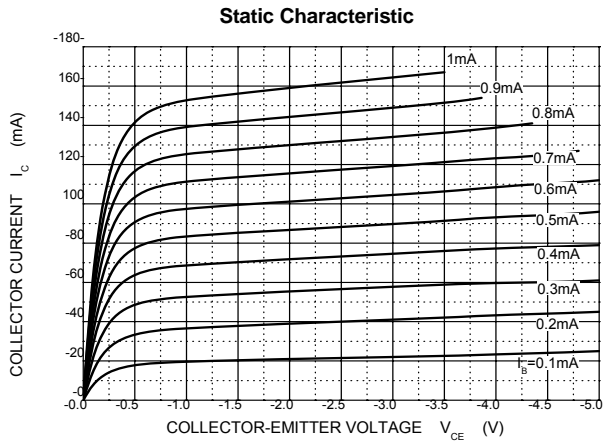
Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	-40	V
$V_{CEO}$	Collector-Emitter Voltage	-25	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_c$	Collector Current -Continuous	-1.5	A
$P_c$	Collector Power Dissipation	0.3	W
$T_j$	Junction Temperature	150	$^{\circ}C$
$T_{stg}$	Storage Temperature	-55-150	$^{\circ}C$

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## ELECTRICAL CHARACTERISTICS (T<sub>amb</sub>=25°C unless otherwise specified)

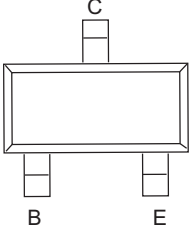
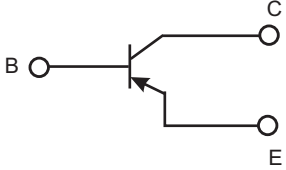
Parameter	Symbol	Test conditions	MIN	MAX	UNIT
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C=-100\mu A, I_E=0$	-40		V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C=-0.1mA, I_B=0$	-25		V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E=-100\mu A, I_C=0$	-5		V
Collector cut-off current	$I_{CBO}$	$V_{CB}=-40V, I_E=0$		-0.1	$\mu A$
Collector cut-off current	$I_{CEO}$	$V_{CE}=-20V, I_B=0$		-0.1	$\mu A$
Emitter cut-off current	$I_{EBO}$	$V_{EB}=-5V, I_C=0$		-0.1	$\mu A$
DC current gain	$h_{FE(1)}$	$V_{CE}=-1V, I_C=-100mA$	200	350	
	$h_{FE(2)}$	$V_{CE}=-1V, I_C=-800mA$	40		
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=-800mA, I_B=-80mA$		-0.5	V
Base-emitter saturation voltage	$V_{BE(sat)}$	$I_C=-800mA, I_B=-80mA$		-1.2	V
Base-emitter on voltage	$V_{BE(on)}$	$I_C=-1V, V_{CE}=-10mA$		-1	V
Base-emitter positive favor voltage	$V_{BEF}$	$I_B=-1A$		-1.55	V
Transition frequency	$f_T$	$V_{CE}=-10V, I_C=-50mA$ $f=30MHz$	100		MHz
output capacitance	$C_{ob}$	$(V_{CB}=-10V, I_E=0, f=1MHz)$		20	pF

# Rating and characteristic curves



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## Pinning information

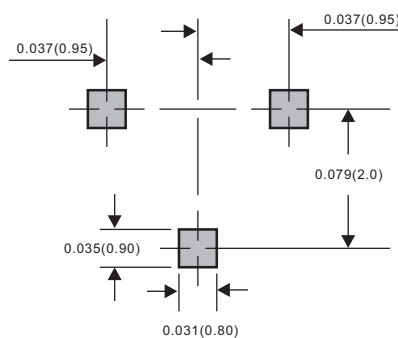
Pin	Simplified outline	Symbol
PinB Base PinC Collector PinE Emitter		

## Marking

Type number	Marking code
HSS8550	Y2

## Suggested solder pad layout

SOT-23



Dimensions in inches and (millimeters)